PLEASE AMEND THE APPLICATION TO READ AS FOLLOWS:

Please change the title of the Application to "SYSTEM AND METHOD FOR CONTROLLING THE ENTRY OF INSTRUCTIONS INTO A PIPELINE OF AN

5   INSTRUCTION PROCESSOR".

IN THE SPECIFICATION:

10   Please amend the Abstract of the Invention with this paragraph for the paragraph that appears on page 44.

A programmable pipeline depth controller is provided to control the number of instructions that begins execution within an instruction pipeline of an instruction processor

15   within a predetermined period of time. The pipeline depth controller of the present invention includes a logic sequencer responsive to a programmable count value. Upon being enabled, the logic sequencer generates a pipeline control signal to selectively delay the entry of some instructions into the instruction pipeline so that the number of instructions that begins execution within the instruction pipeline during the predetermined period of time following the enabling of

20   the logic sequencer is equal to the count value.

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